

REMARKS

Claims 1-37 are pending in this application, with claims 1, 9, 21, 28, and 33 being independent. Claim 21 has been amended.

With respect to the objection to the drawings for not showing the connection electrode or the source wiring, Applicants submit that the connection electrode is clearly shown at least by reference numeral 165 in Fig. 4. Similarly, the source wiring is clearly shown at least by reference numeral 137 in Fig. 4. For these reasons, Applicants request withdrawal of the objection to the drawings.

Claims 1-37 have been rejected as being anticipated by Ohtani. Applicants request reconsideration and withdrawal of this rejection because Ohtani does not describe or suggest an arrangement in which a source wiring and at least one electrode are formed on a first insulating film, a second insulating film covers the electrode and the source wiring, and a gate wiring is formed on the second insulating film and connected to the electrode, as recited in each of the independent claims.

The action indicates that Ohtani teaches a source wiring (source wiring line 816 in Fig. 8) formed on the first insulating film (gate insulating film 807 in Fig. 8). However, as can be seen from Fig. 8, the source wiring 816 is not formed on the first insulating film 807.

In addition, the action indicates that Ohtani teaches a gate wiring (first wiring line 902 in Fig. 9) formed on the second insulating-film (interlay insulating film 820 in Fig. 8). Applicants respectfully disagree. Neither Fig. 9 nor the text of Ohtani shows the gate wiring 902 being formed on the film 820. Moreover, Fig. 9 illustrates an embodiment different from the embodiment of Fig. 8.

For the reasons noted above, Applicants request withdrawal of the rejection of claims 1-37.